



Wireless Power Transfer for Medical Microsystems

By Tianjia Sun, Xiang Xie, Zhihua Wang

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This book provides an in-depth introduction to the newest technologies for designing wireless power transfer systems for medical applications. The authors present a systematic classification of the various types of wireless power transfer, with a focus on inductive power coupling. Readers will learn to overcome many challenges faced in the design a wirelessly powered implant, such as power transfer efficiency, power stability, and the size of power antennas and circuits. This book focuses exclusively on medical applications of the technology and a batteryless capsule endoscopy system and other, real wirelessly powered systems are used as examples of the techniques described.

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Editorial Review

From the Back Cover

This book equips readers with tools for computer architecture of high performance, low power, and high reliability memory hierarchy in computer systems based on emerging memory technologies, such as STTRAM, PCM, FBDRAM, etc. The techniques described offer advantages of high density, near-zero static power, and immunity to soft errors, which have the potential of overcoming the “memory wall.” The authors discuss memory design from various perspectives: emerging memory technologies are employed in the memory hierarchy with novel architecture modification; hybrid memory structure is introduced to leverage advantages from multiple memory technologies; an analytical model named “Moguls” is introduced to explore quantitatively the optimization design of a memory hierarchy; finally, the vulnerability of the CMPs to radiation-based soft errors is improved by replacing different levels of on-chip memory with STT-RAMs.

- Provides a holistic study of using emerging memory technologies in different levels of the memory hierarchy;
- Equips readers with techniques for memory design with improved performance, energy consumption, and reliability;
- Includes coverage of all memory levels, ranging from cache to storage;
- Explains how to choose the proper memory technologies in different levels of the memory hierarchy.

About the Author

Tianjia Sun is a PHD candidate at Tsinghua University. Xiang Xie is an Associated Professor with the Institute of Microelectronics, at Tsinghua University. Zhihua Wang is a Professor of Electronic Engineering, and Deputy Director of the Institute of Microelectronics, at Tsinghua University.

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Mikel Davis:

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